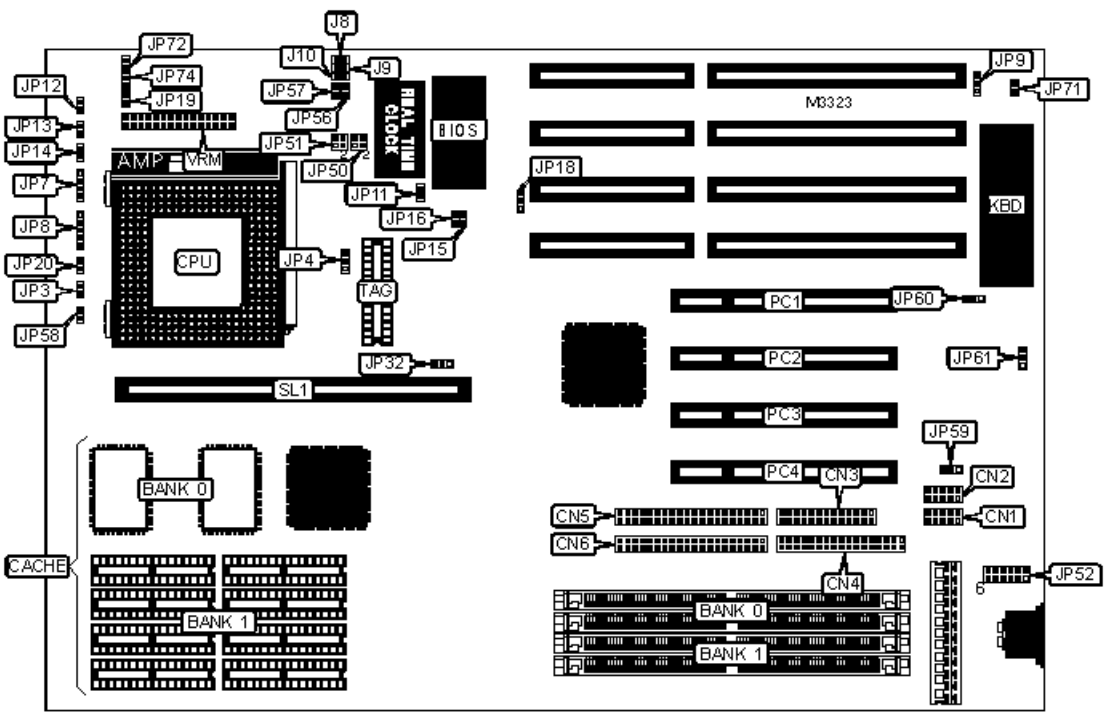


SHUTTLE COMPUTER INTERNATIONAL, INC.

HOT-541 (REV. 1)

Processor	Pentium
Processor Speed	75/90/100/120/133MHz
Chip Set	Intel
Maximum Onboard Memory	128MB (EDO supported)
Cache	256/512KB
BIOS	AMI/Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VRM connector, cache slot
NPU Options	None



CONNECTIONS			
Function	Label	Function	Label
Serial port 1	CN1	Turbo switch	JP13
Serial port 2	CN2	Turbo LED	JP14
Parallel port	CN3	Green PC connector	JP19
Floppy drive interface	CN4	IDE interface LED	JP20
IDE interface 2	CN5	PS/2 mouse interface	JP52

IDE interface 1	CN6	Green PC LED	JP74
Speaker	JP7	32-bit PCI slots	PC1 - PC4
Power LED & keylock	JP8	VRM connector	VRM
Reset switch	JP12	Cache slot	SL1

USER CONFIGURABLE SETTINGS

Setting		Label	Position
»	Flash BIOS voltage select 5v	JP9	Open
	Flash BIOS voltage select 12v	JP9	Pins 2 & 3 closed
»	CMOS memory normal operation	JP11	Open
	CMOS memory clear	JP11	Closed
»	On board I/O enabled	JP59	Pins 1 & 2 closed
	On board I/O disabled	JP59	Pins 2 & 3 closed
»	Monitor type select color	JP71	Closed
	Monitor type select monochrome	JP71	Open
»	Password normal operation	JP72	Open
	Password clear	JP72	Closed

DRAM

Size	Bank 0	Bank 1
8MB	(2) 1M x 32	None
8MB	None	(2) 1M x 32
16MB	(2) 2M x 32	None
16MB	None	(2) 2M x 32
16MB	(2) 1M x 32	(2) 1M x 32
24MB	(2) 1M x 32	(2) 2M x 32

24MB	(2) 2M x 32	(2) 1M x 32
32MB	(2) 4M x 32	None
32MB	None	(2) 4M x 32
32MB	(2) 2M x 32	(2) 2M x 32
40MB	(2) 1M x 32	(2) 4M x 32
40MB	(2) 4M x 32	(2) 1M x 32
48MB	(2) 2M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
64MB	(2) 8M x 32	None
64MB	None	(2) 8M x 32

DRAM (CON'T)		
Size	Bank 0	Bank 1
64MB	(2) 4M x 32	(2) 4M x 32
72MB	(2) 1M x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
80MB	(2) 2M x 32	(2) 8M x 32
80MB	(2) 8M x 32	(2) 2M x 32
96MB	(2) 4M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
128MB	(2) 8M x 32	(2) 8M x 32

Note: Board accepts EDO memory.

CACHE SIZE				
Size	Bank 0	Bank 1	TAG	SL1
256KB (A)	None	(8) 32K x 8	(1) 32K x 8	Not installed

256KB (B)	None	None	None	Installed
256KB (C)	(2) 32K x 32	None	(1) 32K x 8	Not installed
512KB (A)	None	(8) 64K x 8	(1) 32K x 8	Not installed

CACHE JUMPER	
Size	JP4
256KB (A)	Pins 2 & 3 closed
512KB (A)	Pins 1 & 2 closed

CACHE VOLTAGE	
Setting	JP32
Mixed mode	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed

CPU SPEED		
Setting	JP15	JP16
75MHz	Open	Open
90MHz	Closed	Closed
100MHz	Open	Closed
120MHz	Closed	Closed
133MHz	Open	Closed

AT BUS CLOCK SPEED		
System clock	AT bus clock	JP18
50MHz	6.25MHz	Pins 2 & 3 closed
50MHz	8.33MHz	Pins 1 & 2 closed
60MHz	7.5MHz	Pins 2 & 3 closed

60MHz	10MHz	Pins 1 & 2 closed
66MHz	8.25MHz	Pins 2 & 3 closed
66MHz	11MHz	Pins 1 & 2 closed

CPU MULTIPLIER		
Setting	JP3	JP58
1.5x	Open	Open
2x	Closed	Open
2.5x	Closed	Closed
3x	Open	Closed

CPU VOLTAGE			
Setting	J8	J9	J10
3.3v	Closed	Open	Open
3.4v	Open	Closed	Open
3.6v	Open	Open	Closed

VRM CONNECTOR				
Setting	JP50	JP51	JP56	JP57
On board regulator installed	1 & 3, 2 & 4	Open	Closed	Closed
Add on VRM installed at VRM	Open	1 & 3, 2 & 4	Open	Open
On board regulator and add on VRM installed at VRM	Open	Open	Closed	Closed

Note: Pins designated should be in the closed position.

DMA CHANNEL		
Setting	JP60	JP61
DMA1	Pins 1 & 2 closed	Pins 1 & 2 closed

DMA3	Pins 2 & 3 closed	Pins 2 & 3 closed
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PS/2 MOUSE	
Setting	JP52
12-pin header	Pins 2, 3, 4, 5, 6, 8, 9, 10, 11, 12 closed
6-pin mini	Pins 1 - 6 closed